## APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 02-26-2004

Application Type:: Regular Subject Matter:: Utility

Memory Cells Enhanced for Resistance Title::

to Single Event Upset

No

X-1026-2D US Attorney Docket Number::

Request for Early Pub?:: No Request for Non-Pub?:: No Total Drawing Sheets:: 09 Small Entity?:: No Petition included?::

APPLICANT INFORMATION

Applicant Authority Type:: Inventor

Primary Citizenship Ctry:: US

Status:: Full Capacity

Given Name:: Austin

Middle Name:: Η.

Family Name:: Lesea

Street:: 25542 Mt. Bache Road

Los Gatos City:: California State or Province::

Postal or Zip Code:: 95033

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number:: 24309

## DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Divisional	10/125,666	04-17-02

## ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.

Street:: 2100 Logic Drive

City:: San Jose

State or Province:: California

Postal or Zip Code:: 95124